

FIG. 2

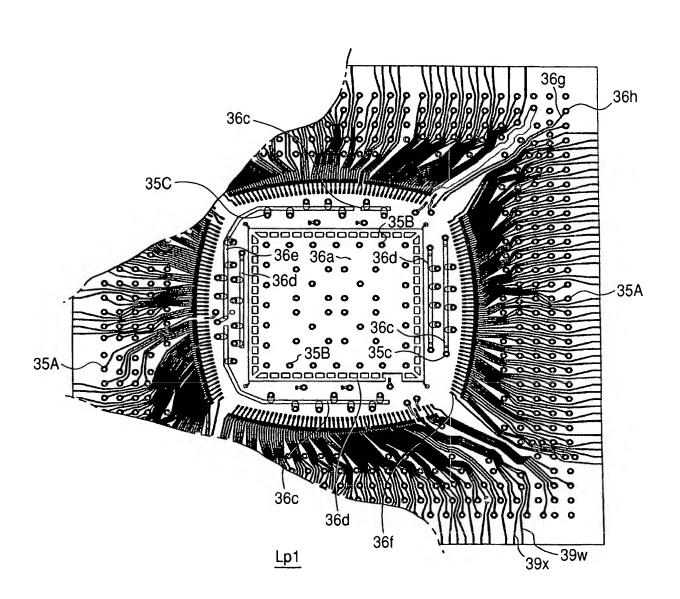


FIG. 3

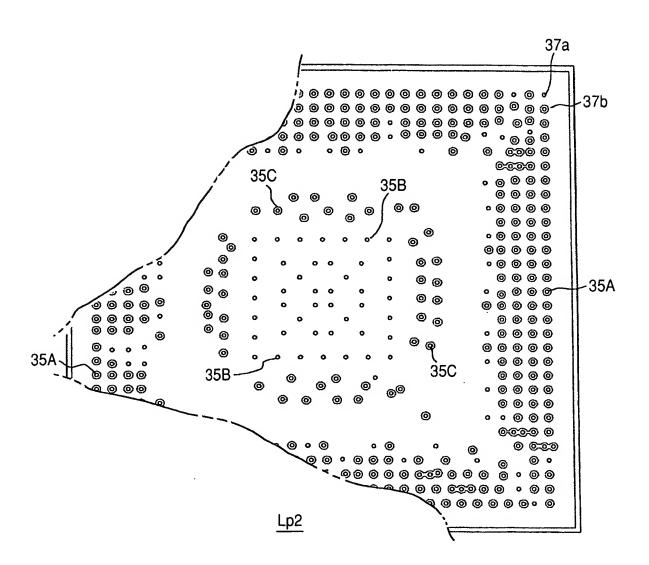


FIG. 4

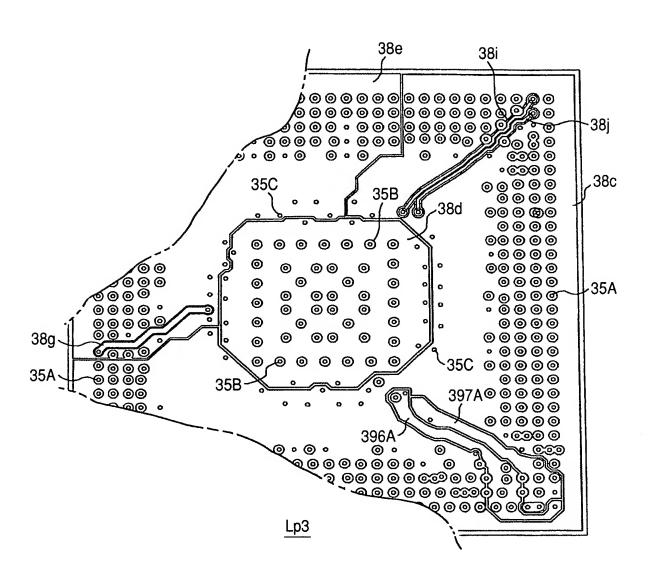
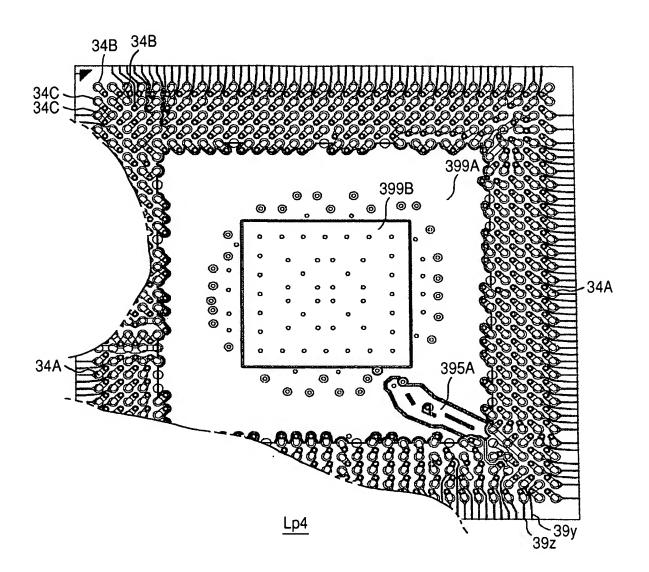


FIG. 5



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FIG. 6

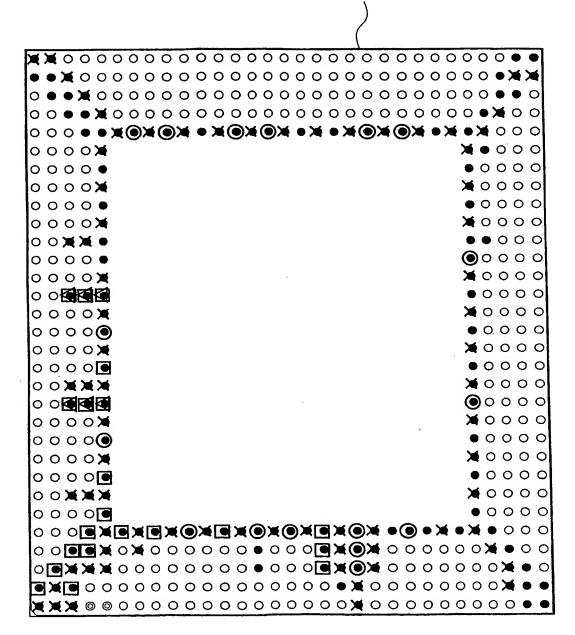
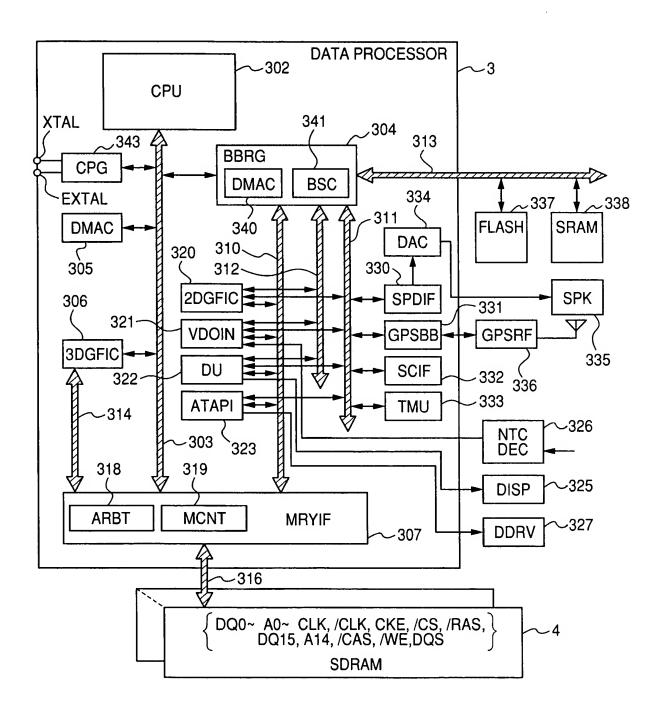
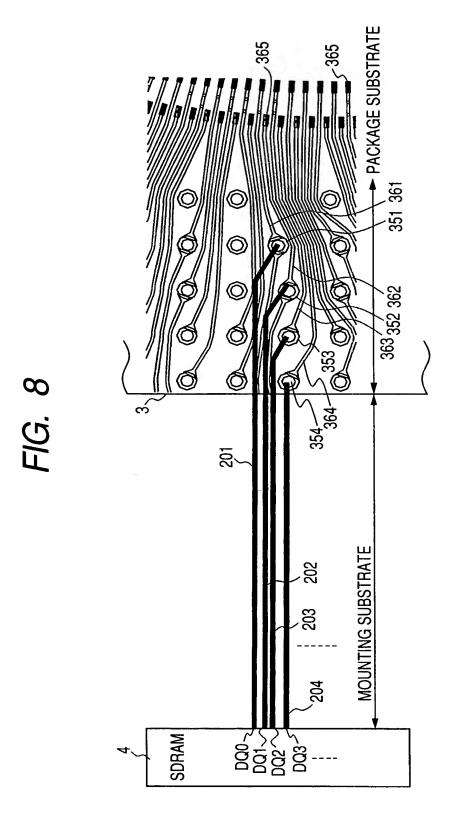


FIG. 7





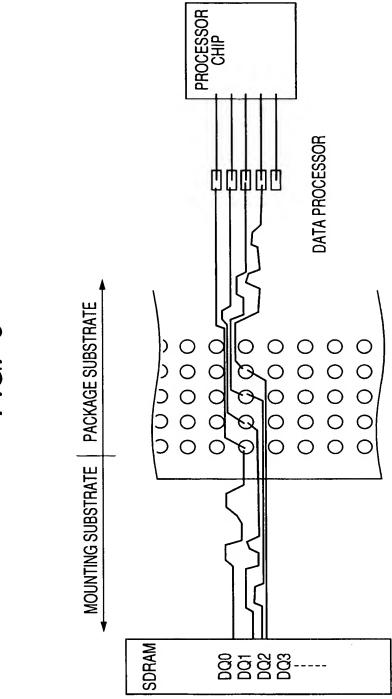
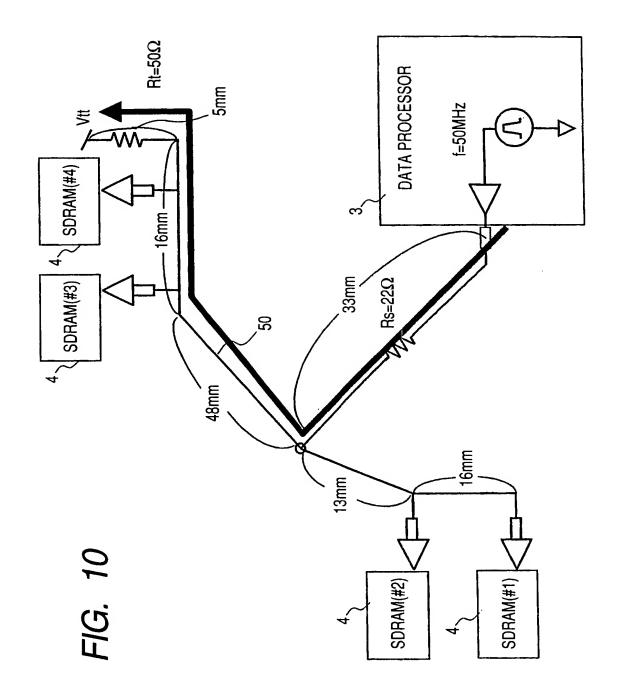


FIG. 9





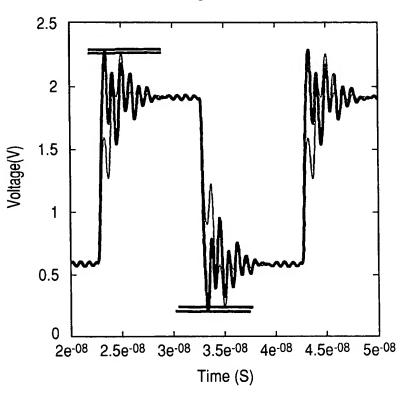


FIG. 12

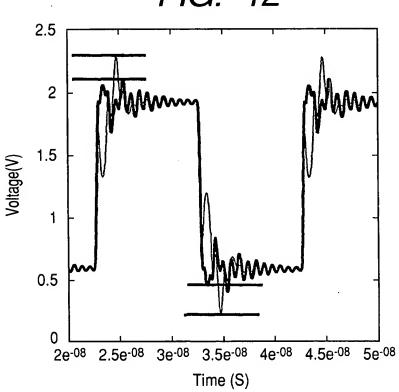


FIG. 13

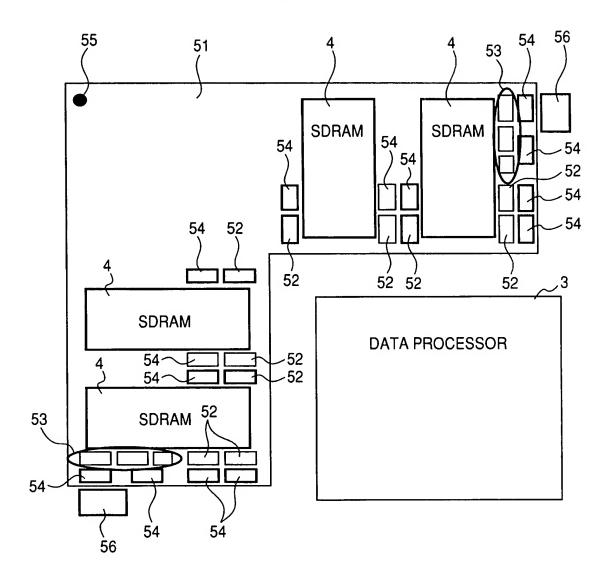
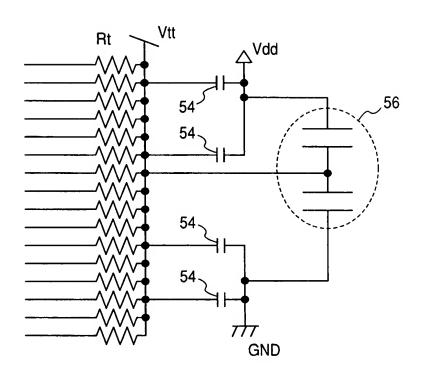


FIG. 14



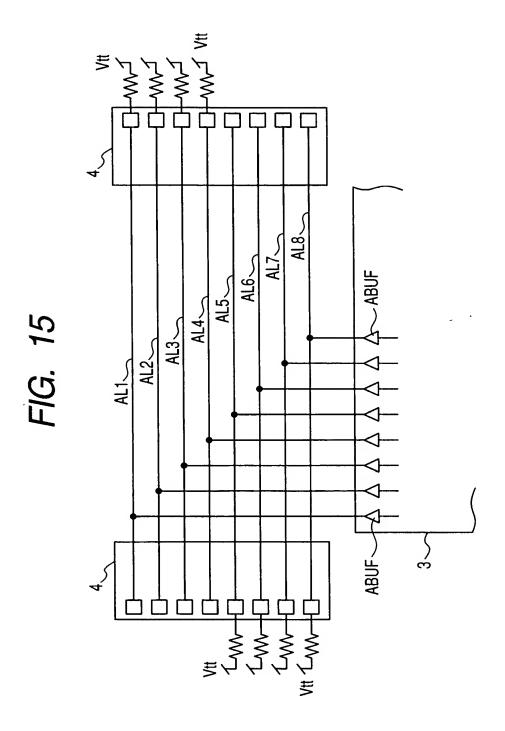


FIG. 16

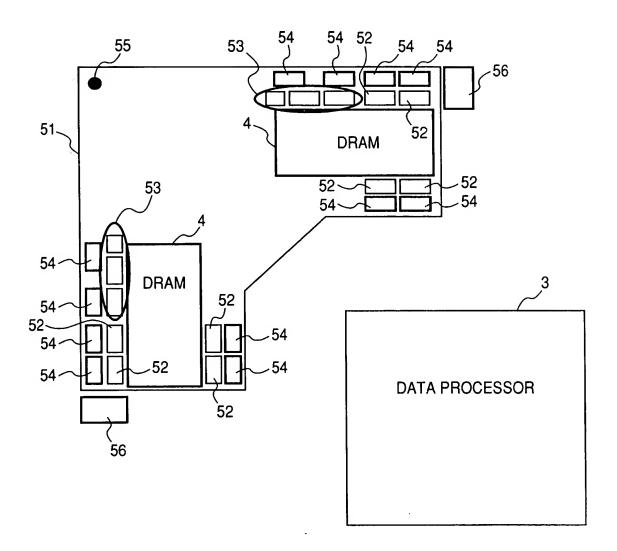


FIG. 17

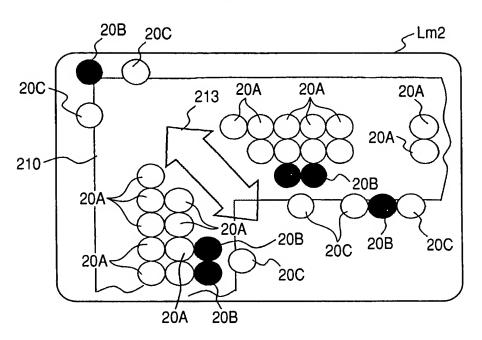


FIG. 18

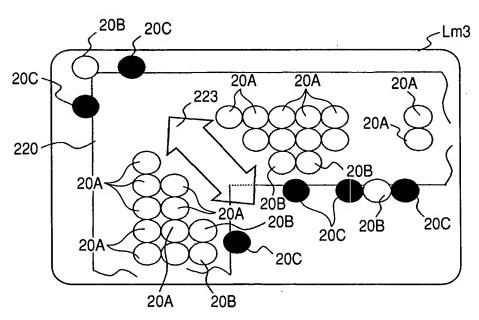


FIG. 19

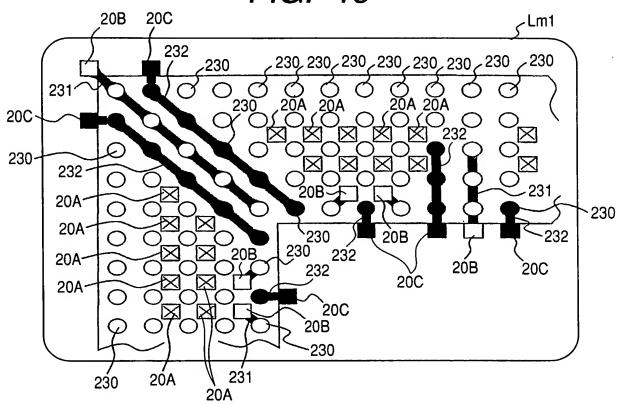


FIG. 20

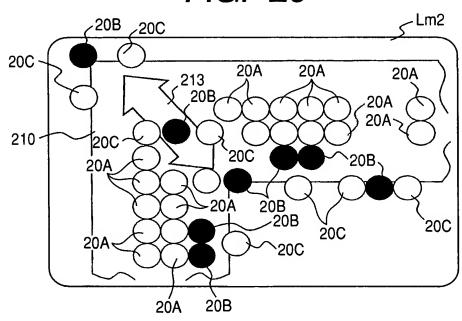


FIG. 21 _Lm3 20B ~20C 223 20B 20C 20A 20A 20A 220-20C 20C ~20B 20A 20B ___20B >20A 20B 20C 20C 20A-`20C 20A 20B

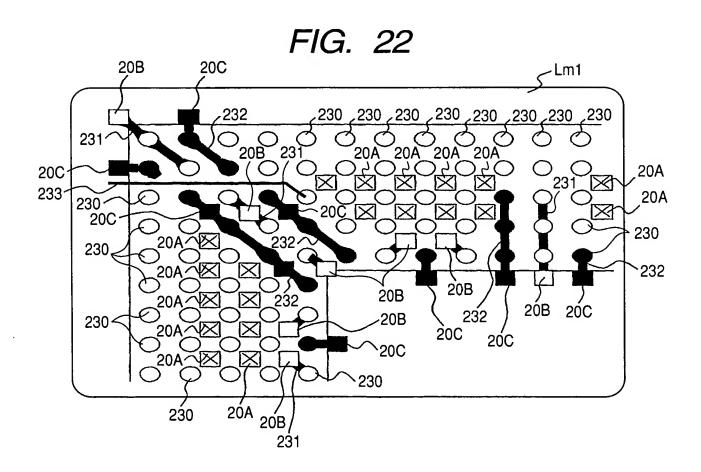
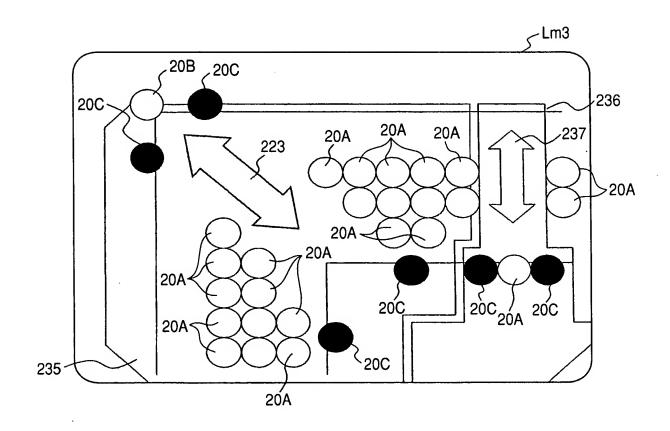


FIG. 23



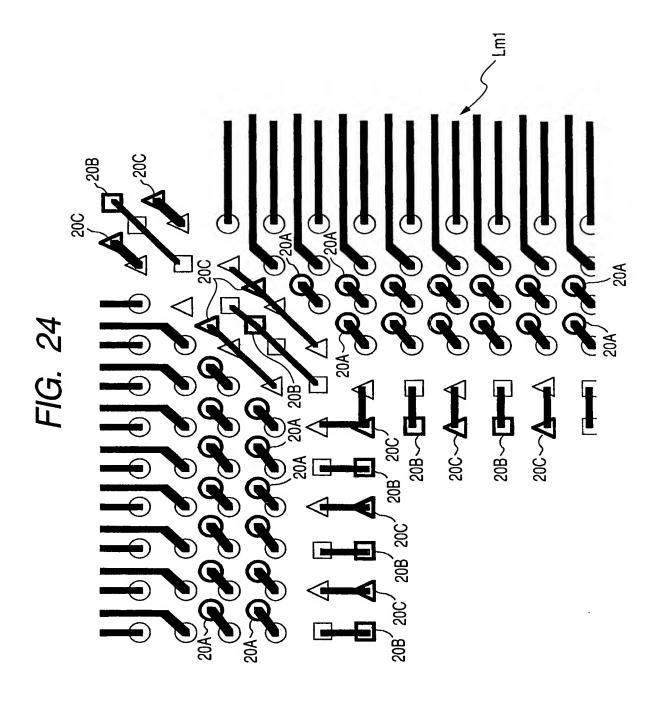


FIG. 25

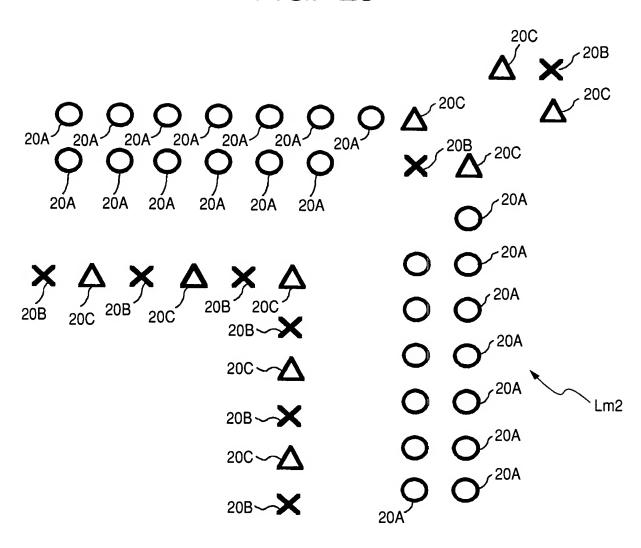


FIG. 26

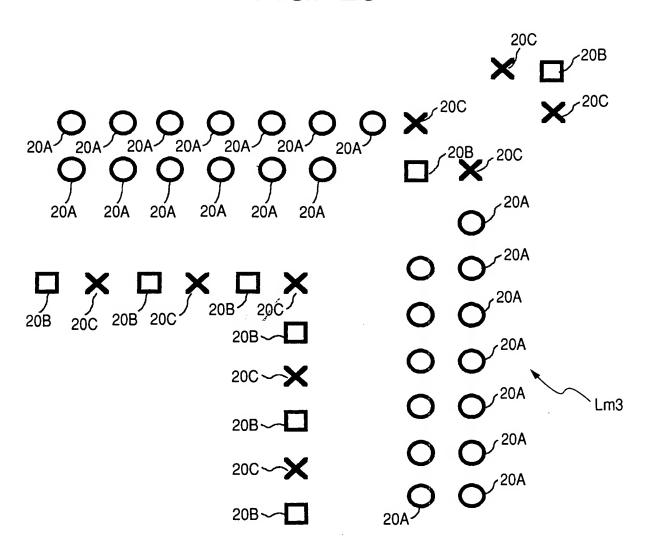


FIG. 27

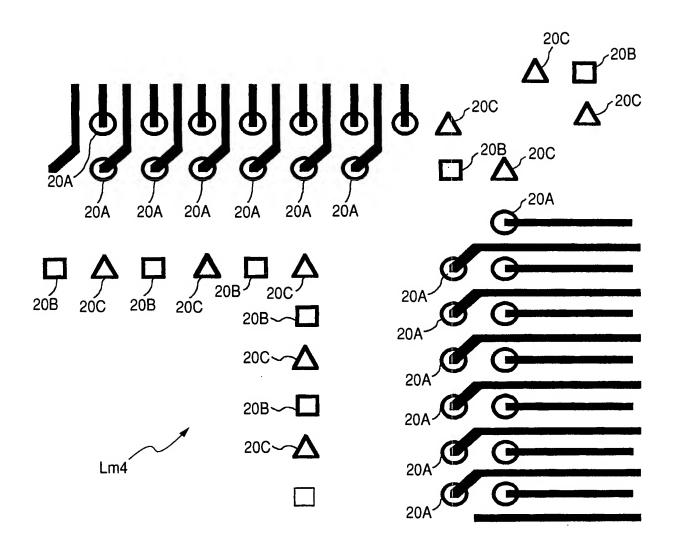


FIG. 28

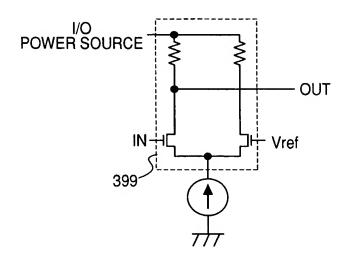


FIG. 31

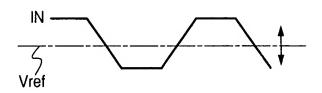
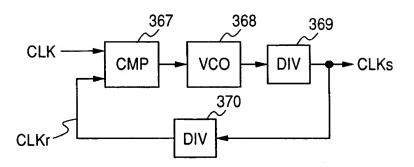
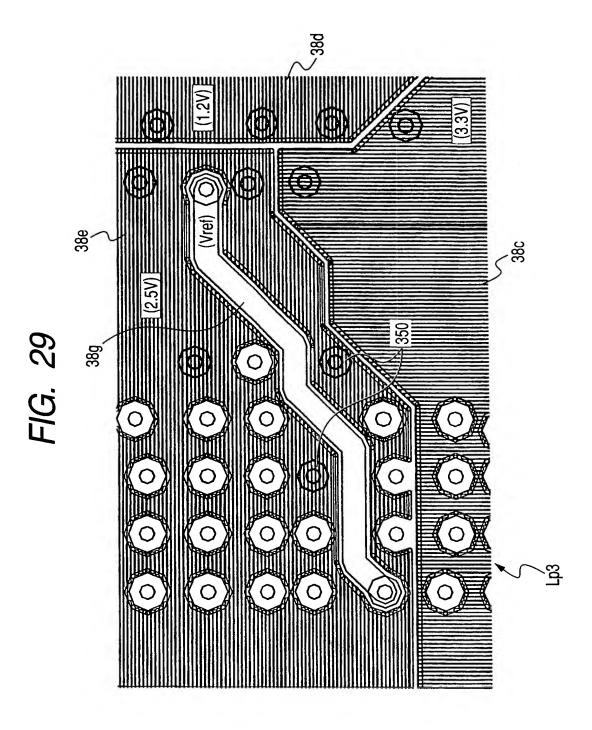
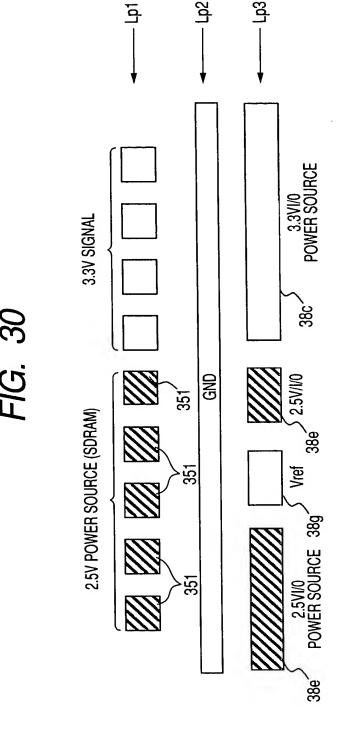
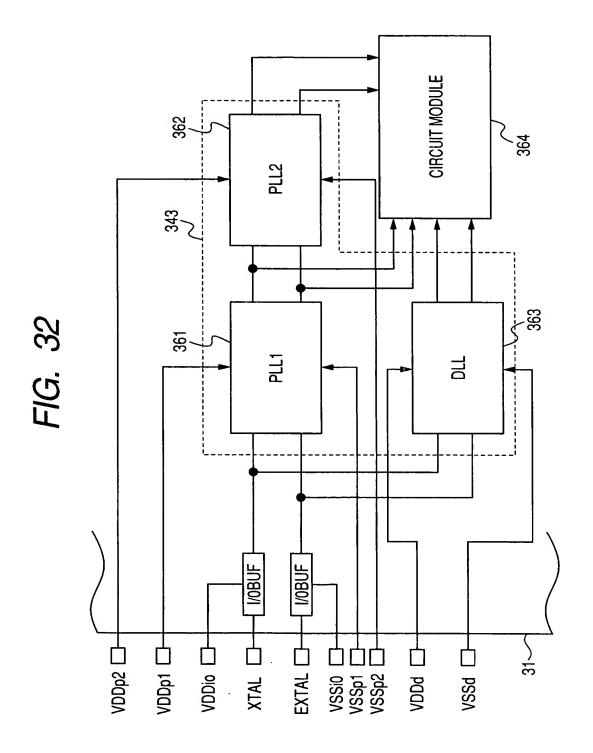


FIG. 33



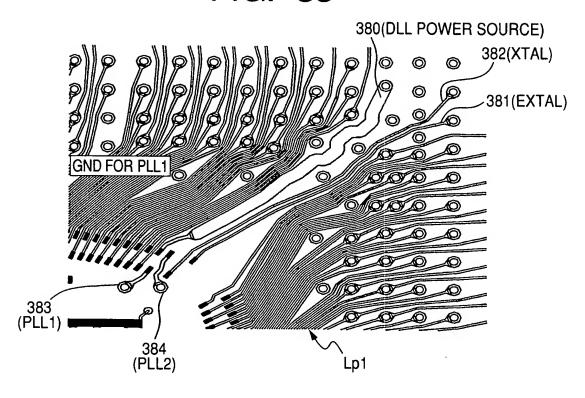


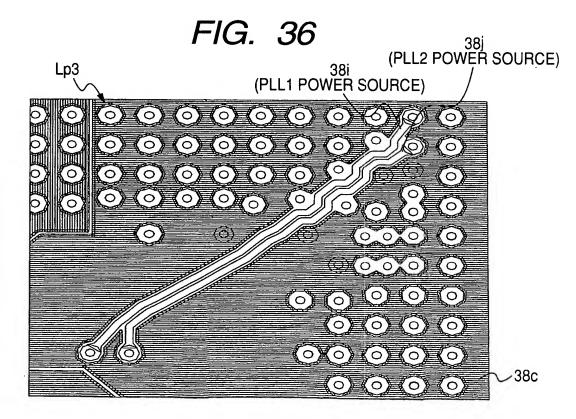




(I/0 POWER SOURCE) 380 .38 (DLL POWER SOURCE) GND (ANOTHER SIGNAL) (I/O POWER SOURCE) Lp2-

FIG. 35





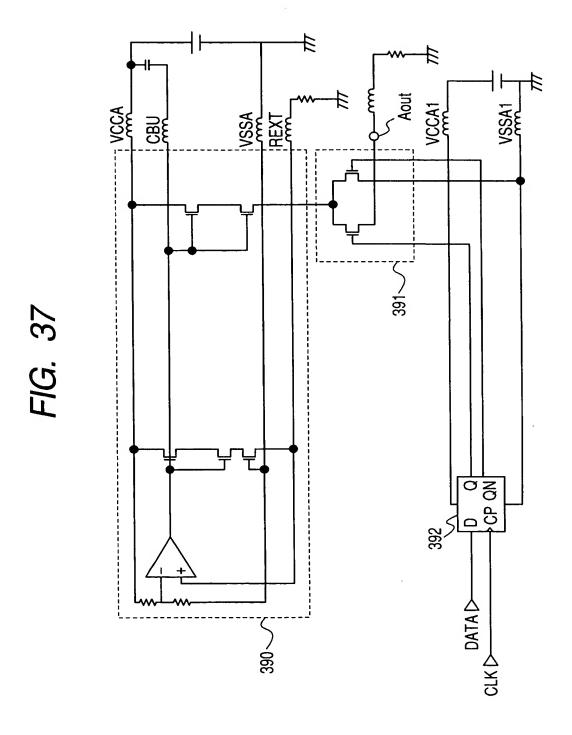


FIG. 38

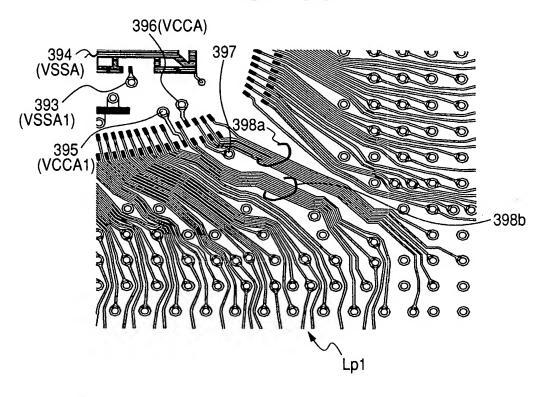
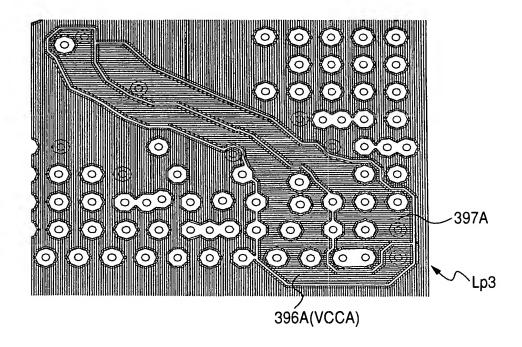


FIG. 40



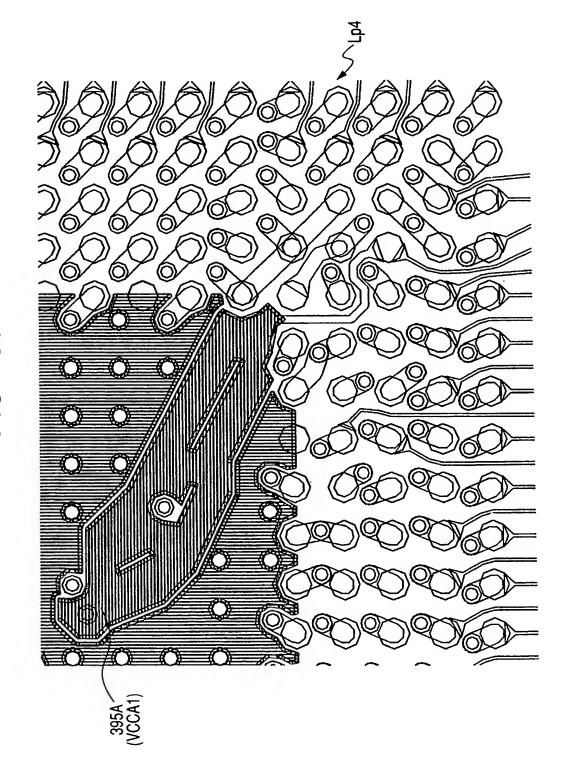


FIG. 39

FIG. 41

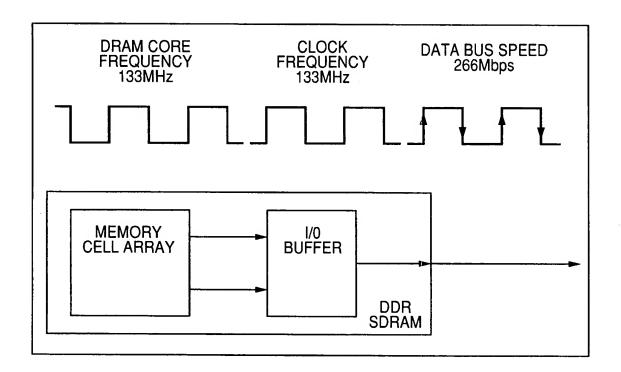
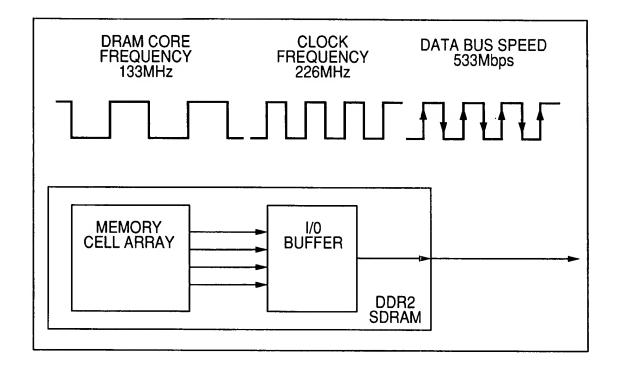


FIG. 42



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